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June 18, 2008

**Customer No. 66785**

Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Re: Applicant(s): Chen et al.  
Title: Read and Erase Verify Methods and Circuits Suitable for Low Voltage Non-Volatile Memories  
Application No.: 10/552,948 Filing Date: September 11, 2006  
Examiner: Yoha, Connie C. Group Art Unit: 2827  
Docket No.: SNDK.284US1 Conf. No.: 6573

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

- (1) This Transmittal Letter (1 page);
- (2) Interview Summary (2 pages); and
- (3) Terminal Disclaimer (2 pages).

- ☐ No additional fee is required.  
☒ The fee has been calculated as shown below:

<input checked="" type="checkbox"/>	Fee for Terminal Disclaimer	130.00
<b>Total additional fee:</b>		<b>\$ 130.00</b>

- ☒ The fee of \$130.00 has been authorized via EFS to Deposit Account 040258. The Commissioner is hereby authorized to charge any additional fees, which may be required, or credit any overpayment to Deposit Account 040258.

**FILED VIA EFS**

Respectfully submitted,

Michael G. Cleveland  
Reg. No. 46,030